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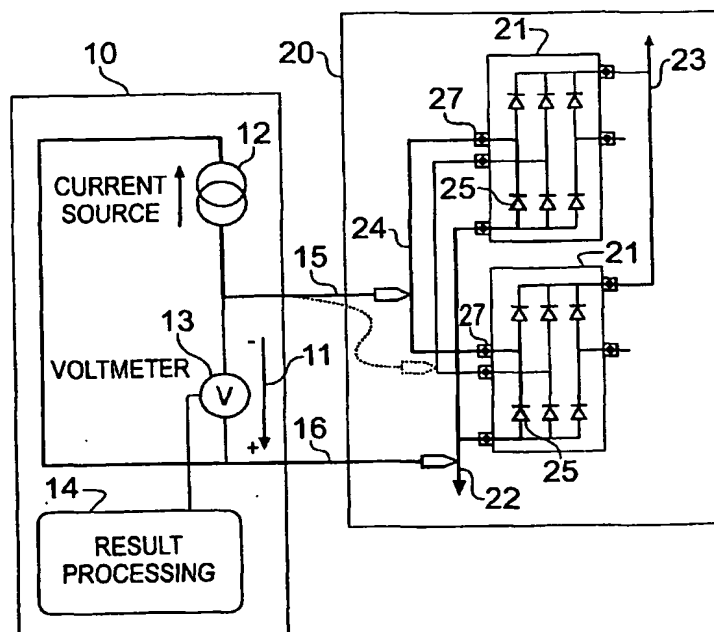
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(54) Title: A METHOD OF AND APPARATUS FOR TESTING FOR INTEGRATED CIRCUIT CONTACT DEFECTS



(57) Abstract: Various tester configurations are provided that injects test signals into nets (e.g. 24). Non-linear characteristics of the response are detected (e.g. harmonics, do offset) and used to assess the adequacy or otherwise of device connections in the net.